



Numem to Present at Flash Memory Summit 2020

A New Spin on Embedded Memory

Sunnyvale, Calif, November 2, 2020 – Numem, the leading provider of NuRAM & SmartMem Smart Compute Memory IP Cores based on MRAM will be presenting at the Flash Memory Summit Virtual Conference & Expo on November 10, 2020.

Jack Guedj, CEO and Co-Founder will present ***“MRAM-based DNN Accelerator for High Performance Space Computing”*** as the keynote from 8:00 – 8:30 a.m.

The presentation will cover the migration from centralized cloud to distributed processing and the relevance for Space applications. It will also provide an overview of the architecture and benefits of the solution both in terms of Power Reduction and Memory die area, and cover some of the radiation testing done on NuRAM.

Event: 15th. Annual Flash Memory Summit Virtual Conference & Expo, November 10–12, 2020 <https://flashmemorysummit.com/>

About Numem

Numem headquartered in Sunnyvale, California, is the leading provider of advanced Smart Compute Memory IP Cores based on its patented NuRAM and “SmartMem” technologies. Leveraging 50+ years’ experience in MRAM/Complex memory designs, test chips, and data collected on thousands of devices, Numem NuRAM provides an excellent SRAM and eFlash alternative to SoC Designers and Architects providing 2.5x to 3.5x smaller area and >20x lower leakage power while providing read performance that is 2x to 5x faster than commercially available MRAM. Numem NuRAM can also aggregate ROM, EEPROM and OTP into a single, unified memory with SRAM like interfacing for ease of integration and SW programmability.